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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Tews, *et al.* Docket No.: 2004 P 51343 US
Serial No.: 10/840,146 Art Unit: 2814
Filed: May 6, 2004 Examiner: Shrinivas H. Rao
For: Semiconductor Structures and Manufacturing Method

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Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to examination on the merits, Applicant respectfully submits this Preliminary
Amendment as set forth below: